

**A.GUNASUNDARI**

**Assistant Professor**

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## **EDUCATION**

### **POSTGRADUATE**

MAY 2008  
ME- Applied Electronics  
PSG College of Technology  
Bharathiar University

### **UNDERGRADUATE**

(Month/Year)  
BE- EEE  
Government College of Technology  
Bharathiar University

## **EXPERIENCE**

21/07/2001 to 1/03/2004      Lecturer  
PSG Polytechnic College

1/03/2004 to 31/12/2005      Lecturer  
PSG College of Technology

1/01/2006 to till date      Assistant Professor  
PSG College of Technology

## **COURSES HANDLED**

### **UNDERGRADUATE**

1. Electronics Devices
2. Electronics Circuits
3. VLSI Design
4. Basics of Electrical and Electronics Engineering
5. Electric drives and controls
6. Microcontrollers and PLC

## POSTGRADUATE

1. Digital Image Processing
2. VLSI Design
3. Wavelets & Applications
4. Microcontrollers & its Applications

## RESEARCH AREAS

VLSI Testing

## PUBLICATIONS

### INTERNATIONAL JOURNALS

- A.Gunasundari, and T Rafeek ,” *Reliable Non invasive First Trimester Screening Test Using Image processing and Artificial Neural Network*” International Journal of Engineering Research and Applications (IJERA) ISSN: 2248-9622.
- A.Gunasundari, Dr. M.C. Bhuvaneshwari and K.R. Sri Sountharya , “*Mixed Signal Integrated Circuits Testing Using Wavelets And Neural Networks* Australian Journal of Basic and Applied Sciences ISSN:1991-8178.

### NATIONAL CONFERENCES

- V.Divya, Dr.M.C.Bhuvaneshwari, A.Gunasundari, “ Fault Detection In Analog And Mixed Signal Circuits Using Hilbert Haung Transform” by Proceedings of the National Conference on NCDTAEPS’16. May 5-6, 2016.
- Sevandhi P, Mrs. A. Gunasundari, Dr. M.C. Bhuvaneshwari, “Fault Detection in circuits using Support Vector Machine” by Proceedings of the National Conference on NCDTAEPS’16. May 5-6, 2016.
- Mr.K,Siva Kumar, Mrs.A.Gunasundari . “Fault Diagnosis of Analog Circuits Using WNN by Proceedings of the National Conference on NCDTAEPS’16. May 5-6, 2016.
- ThamaraiKannan.T, Gunasundari A , “*Fault Detection and Identification in Analog Circuits using Genetic Algorithm*” by Proceedings of the National Conference on NCDTAEPS’15. May 8-9, 2015.
- Sri Sountharya K.R, and Gunasundari A , “Fault Diagnosis of Mixed Signal Integrated Circuits” Proceedings of the National Conference on NCDTAEPS’15. May 8-9, 2015 of Basic and Applied Sciences, 2015.
- R.Arunkumar, A.Gunasundari,”*Neural network based Analog circuit Fault diagnosis*” ,*Proceedings of National Conference on Recent trends in and Electronics Engineering* May- 2014.

## **SEMINARS / CONFERENCES ORGANISED**

Two days National Level workshop on “VLSI LAYOUT DESIGN” during March 21-22, 2014.

## **SEMINARS / CONFERENCES ATTENDED**

- AICTE sponsored two week SDP on “ Orientation of teachers in the area of DSP Applications” organised by Department of EEE KCT, Coimbatore during 6<sup>th</sup> to 19<sup>th</sup> November 2005.
- AICTE sponsored two week SDP on “Imparting Learner Training Skills for Engineering Faculty” organised by Department of English, PSGCT, Coimbatore during January 19<sup>th</sup> to February 2<sup>nd</sup> 2007.
- Workshop on “E-Learning” organised by Multimedia Lab PSGCT, Coimbatore during March 27-28, 2008.
- Workshop on “Wireless and Digital Measurements” Jointly organised by Department of ECE PSGCT, Coimbatore and M/s Agilent Technologies during 26<sup>th</sup> & 27<sup>th</sup> February, 2010.
- Workshop on Research methods organised by Department of BME, PSGCT, Coimbatore during 1<sup>st</sup> & 2<sup>nd</sup> March, 2010.
- NPTEL workshop organised by IIT Madras during April 5<sup>th</sup> -9<sup>th</sup>, 2010.
- Workshop on “VLSI DESIGN” organised by Department of CSE, PSGCT and Advanced Computing and Communications Society (ACCS) Coimbatore chapter during August, 12<sup>th</sup> – 13<sup>th</sup> 2010.
- Two day National seminar on “Strategies in Enhancing Research”, organised by Centre for Research. Anna University of Technology, Coimbatore during 16<sup>th</sup> & 17<sup>th</sup> of February, 2012.
- AICTE sponsored two week SDP on “Research challenges Emerging Trends in Intelligent Information Retrieval”, organised by Department of Mathematics and Computer Applications PSGCT, Coimbatore during 21<sup>st</sup> April – 5<sup>th</sup> May, 2012.

- TEQIP sponsored one week FDP on “2D Signal Processing using Wavelets” organised by Department of ECE ,GCT, Coimbatore during 12<sup>th</sup> – 18<sup>th</sup> September ,2012
- TEQIP sponsored one week FDP on “Engineering Solutions Through MATLAB” organised by Department of EEE, GCT, Coimbatore during 11<sup>th</sup> -17<sup>th</sup> during October, 2012.
- CSIR sponsored National Workshop on “ Fuzzy Systems and soft computing in Industrial Applications” organised by Department of Mathematics, PSGCT, during 21<sup>st</sup>-22<sup>nd</sup> December ,2012
- AICTE sponsored two week SDP on “Emerging Trends in computing – An academic, Research and Social Perspective” organised by Department of CSE, PSGCT, Coimbatore during May 13<sup>th</sup>- 26<sup>th</sup> 2013.
- Two weeks ISTE Workshop on “Analog Electronics”, conducted by IIT, Kharagpur during 4<sup>th</sup>- 14<sup>th</sup>June, 2013.
- TEQIP sponsored one week FDP on “Analysis of Electrical and Electronics Systems using MATLAB and PSPICE ” organised by Department of EEE, PSGCT, Coimbatore during 20<sup>th</sup>- 26<sup>th</sup>June,2013.
- Two Day Seminar on “Latest Developments in Solar Photo Voltaic Technology” organised by PSG Institute of Advanced Studies, Coimbatore in collaboration with University of Albany, State University of New York, USA during 7<sup>th</sup>- 8<sup>th</sup> August, 2013.
- AICTE sponsored two week SDP on “Verification and Testing of VLSI circuits” organised by Department of ECE, PSGCT, Coimbatore during 6<sup>th</sup>- 19<sup>th</sup> November, 2013.
- TEQIP II sponsored one week FDP on “Digital and Analog VLSI Design” organised by Department of ECE CIT, Coimbatore during 20<sup>th</sup>- 26<sup>th</sup> November,2013.
- TEQIP II sponsored one week FDP on “Embedded and Real time Software & Systems” organised by Department of EEE, PSGCT, Coimbatore during 12<sup>th</sup>- 18<sup>th</sup> October, 2014.
- TEQIP II sponsored one week FDP on “ Real time Embedded Systems and Internet of Things(IoT)” organized by Department of EEE, PSGCT, Coimbatore during 22<sup>nd</sup>-28<sup>th</sup>June,2016.

- TEQIP II sponsored one week FDP on “Engineering Mathematics Through Applications (EMTA 2016)” organised by Department of Mathematics, PSGCT, Coimbatore during 4<sup>th</sup>- 11<sup>th</sup> July, 2016.

### **SPECIAL LECTURES DELIVERED**

- A Research seminar has been presented on the topic “Applications of wavelets in the area of testing of Mixed signal circuits”.

### **AWARDS**

Received Dakshinamoorthy Award for Teaching Excellence in the year 2014-2015.

### **MEMBERSHIP OF PROFESSIONAL BODIES**

**Life Member of ISTE**

**Life Member of ACS**